

**IN MEMORIAM
HELMUT VEITH (1971–2016)**



Born: February 5, 1971, Vienna, Austria **Died:** March 12, 2016, Vienna, Austria

Alma mater: Vienna University of Technology

Doctoral advisor: Georg Gottlob

ABOUT HELMUT

Helmut Veith was an Austrian computer scientist who worked on the areas of software engineering, computer-aided software verification and testing, embedded software, computer security, model checking, and logic in computer science. He was a brilliant researcher, an inspiring collaborator, a stimulating teacher, a mentor, a generous friend, and a wonderful father and husband.

Helmut held a professorship at the Faculty of Informatics of the Vienna University of Technology (TU Vienna), and an adjunct professorship at Carnegie Mellon University. He had a **Diploma in Computational Logic** and a **PhD *sub auspiciis praesidentis*** in Computer Science, both from TU Vienna. Prior to his appointment to Vienna, he held professor positions at TU Darmstadt and TU Munich.

With his co-authors, he received the **CAV Award 2015** honoring contributions of fundamental importance to the field of computer aided verification for his contribution to the development of CEGAR.

His work on the software model checker MAGIC received the **ACM Distinguished Paper Award** for contribution to the study of verification of modular software.

In 2016, he was posthumously awarded an **ERC Advanced Grant** on the topic Harnessing Model Checking Technology for Distributed Algorithms.

SOME ACTIVITIES



Vienna Summer of Logic 2014 (co chair)
FLoC 2014 (chair)
FWF Doctoral College on Logical Methods in Computer Science (Speaker)
FWF Research Network on Rigorous Systems Engineering (Deputy Coordinator)
Austrian Society for Rigorous Systems Engineering (President)
Vienna Center for Logic and Algorithms (co chair)
Kurt Gödel Society (Executive Board)
European Association for Computer Science Logic (EACSL) (Executive Board)
Association for Symbolic Logic, Committee on Logic Education
Academic Senate of TU Vienna (vice speaker of professors)
TU Vienna Research focus in Computational Intelligence (coordinator)
TU Vienna curriculum commission for computer science

Program committee co-chair of FMCAD 2015, CAV 2013, CSL 2010, LPAR 2008;
Tutorial Chair, FMCAD 2010

Program committee member of TACAS 2015, ATVA 2010, CAV 2003, CAV 2005, CAV 2009, CAV 2010, CAV 2012, CSL 2009, CSL 2011, CSL 2012, CSR 2007-2009, DATE 2011, DATE 2012, EC2 2009-2012, FMCAD 2009, FMCAD 2010, FMCAD 2011, FMICS 2011, FSTTCS 2007, FORTE 2013, HVC 2010, HVC 2011, ICTAC 2009, ICTAC 2010, ICTERI 2011, ICTERI 2012, LATA 2013, LICS 2004, SOFSEM 2012, SPIN 2012, SPIN 2013, SYNASC 2008-2011, TACAS SW Verification Competition 2012, TASE 2011, VMCAI 2012, WING 2009, WING 2010, WOLLIC 2011 etc.

Helmut authored **131 Publications** — see <http://forsyte.at/people/veith/>. In his memory, there will be an award to support promising students. Contributions to the **Helmut Veith Award** can be made to “Zentrum für Informatikforschung”, IBAN: AT36 1200 0515 8258 2701, BIC: BKAUATWW, reference: “Helmut Veith Award” or by credit card via PayPal.



Vienna Summer of Logic 2014

It was the largest event in the history of logic (!), consisting of twelve large conferences and numerous workshops, attracting over 2000 researchers from all over the world. See the full report at <http://vsl2014.at/>.

They also gave themselves (and logic) a lot of publicity!



Vertreter: supporters, advocates, representatives

Some Photos from the VSL



Prof. & Mrs. Baaz
Prof. & Mrs. Veith

With Ed Clarke
and ??



FMCAD 2016

Formal Methods in Computer-Aided Design

Mountain View, California, 3-6 October, 2016

There will be a section dedicated to Helmut where five colleagues will talk about him.

see:

<http://www.cs.utexas.edu/users/hunt/FMCAD/FMCAD16/index.shtml>