

Syntax-Guided Synthesis in SMT: A View from Inside the Solver

Andrew Reynolds

March 1, 2021



THE UNIVERSITY
OF IOWA

Satisfiability Modulo Theories (SMT) Solvers

- Fully automated reasoners with many applications
 - Verification, *Synthesis*, Symbolic Execution, Theorem Proving, Security Analysis
- SMT solver CVC4
 - Open source, available at : <https://cvc4.github.io/>
- Acknowledgements:
 - Cesare Tinelli, Clark Barrett, Haniel Barbosa, Andres Noetzli, Aina Niemetz, Mathias Preiner
 - Rest of CVC4 development team (past and present)
 - Viktor Kuncak



Synthesis Conjectures

$$\exists f . \forall x . P (f , x)$$

There exists a function f for which **property** P holds for all x

Synthesis Conjectures *Modulo T*

$$\exists f . \forall x . \mathbf{P} (f, x)$$

There exists a function f for which property P holds for all x

Property P is in **background theory** T , e.g. linear arithmetic

$$\exists f . \forall x . f (x+1) \geq f (x)$$

\Rightarrow Satisfiability Modulo Theories (SMT)

Synthesis Conjectures Modulo T

$$\exists f . \forall x . P (f , x)$$

There exists a function f for which **property** P holds for all x

Syntax-Guided Synthesis Conjectures Modulo T

$$\exists f. \forall x. P(f, x)$$

spec (f)

There exists a function f for which **property** P holds for all x

$$\begin{aligned} A &\rightarrow A+A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A) \\ B &\rightarrow B \wedge B \mid \neg B \mid A=A \mid A \geq A \mid \perp \end{aligned}$$

syntax (f)

The body of f is generated by the above **grammar** with start symbol A

\Rightarrow *Syntax-guided synthesis* “SyGuS” [Alur et al 2013]

Enumerative Cex-Guided Inductive Synthesis (CEGIS)

syntax (f) :

$A \rightarrow A + A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A)$

$B \rightarrow B \wedge B \mid \neg B \mid A = A \mid A \geq A \mid \perp$

spec (f) :

$\forall x y. f(x, y) = f(y, x) + f(0, 1)$

Solution
Enumerator

Solution
Verifier

Enumerative Cex-Guided Inductive Synthesis (CEGIS)

$\text{syntax}(f) :$

$A \rightarrow A + A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A)$

$B \rightarrow B \wedge B \mid \neg B \mid A = A \mid A \geq A \mid \perp$

$\text{spec}(f) :$

$\forall x y. f(x, y) = f(y, x) + f(0, 1)$

Solution
Enumerator

Solution
Verifier

$f := \lambda x y. x?$

Enumerative Cex-Guided Inductive Synthesis (CEGIS)

syntax (f) :

$A \rightarrow A+A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A)$
 $B \rightarrow B \wedge B \mid \neg B \mid A=A \mid A \geq A \mid \perp$

spec (f) :

$\forall xy. f(x, y) = f(y, x) + f(0, 1)$

**Solution
Enumerator**

$f := \lambda xy. 0?$

$f(2, 1) = f(1, 2) + f(0, 1)$

$f := \lambda xy. y?$

$f(0, 1) = f(1, 0) + f(0, 1)$

$f := \lambda xy. x?$

**Solution
Verifier**

$f := \lambda xy. 0$

...new candidate $\lambda xy. 0$ has no **counterexamples** wrt **spec (f)**

Enumerative Cex-Guided Inductive Synthesis (CEGIS)

syntax (f) :

$A \rightarrow A+A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A)$
 $B \rightarrow B \wedge B \mid \neg B \mid A=A \mid A \geq A \mid \perp$

spec (f) :

$\forall xy. f(x, y) = f(y, x) + f(0, 1)$

Solution
Enumerator

$f := \lambda xy. 0?$

$f(2, 1) = f(1, 2) + f(0, 1)$

$f := \lambda xy. y?$

$f(0, 1) = f(1, 0) + f(0, 1)$

$f := \lambda xy. x?$

Solution
Verifier

$f := \lambda xy. 0$

\Rightarrow Terms $x, y, 0, \dots$ are a (fair) **enumeration** of terms generated by **syntax (f)**

CEGIS using SMT solvers

syntax (f) :

$A \rightarrow A + A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A)$

$B \rightarrow B \wedge B \mid \neg B \mid A = A \mid A \geq A \mid \perp$

Solution
Enumerator

spec (f) :

$\forall x y. f(x, y) = f(y, x) + f(0, 1)$

SMT Solver

Solution
Verifier

CEGIS inside an SMT solver

$\text{syntax}(f) :$

$A \rightarrow A + A \mid -A \mid x \mid y \mid 0 \mid 1 \mid \text{ite}(B, A, A)$

$B \rightarrow B \wedge B \mid \neg B \mid A = A \mid A \geq A \mid \perp$

$\text{spec}(f) :$

$\forall x y. f(x, y) = f(y, x) + f(0, 1)$

SMT Solver (CVC4)

[Reynolds et al CAV 2015]

Solution
Enumerator

Solution
Verifier

- ✓ Synthesis algorithms that use internal state of SMT solver
- ✓ Tight integration between enumerator and verifier

In This Talk

- Synthesis approaches used by SMT+SyGuS solver CVC4:
 1. Counterexample-guided quantifier instantiation
 2. Smart Enumerative SyGuS
 3. Fast Enumerative SyGuS
- Internal applications of SyGuS for SMT solvers
- Future work

Synthesis Solver CVC4

$\exists f. \forall x. P(f, x)$

CVC4

?

$f = \lambda x. t(x)$

Synthesis Solvers in CVC4

$$\exists f . \forall x . P (f , x)$$

CVC4

?

Counterexample
Guided QI

[Reynolds et al CAV 2015, FMSD 2017,
Niemetz et al CAV 2018]

Smart
Enumerative

[Reynolds et al CAV 2015, IJCAR 2018]

Fast
Enumerative

[Reynolds et al CAV 2019]

⇒ Best approach to apply depends on the conjecture

Approach #1: Counterexample-Guided Instantiation

Synthesis via Counterexample-Guided Instantiation

- Some synthesis conjectures are *essentially first-order*:

$$\neg \exists f . \forall x y . \mathbf{f}(x, y) \geq x \wedge \mathbf{f}(x, y) \geq y \wedge (\mathbf{f}(x, y) = x \vee \mathbf{f}(x, y) = y)$$

“ $\mathbf{f}(x, y)$ is the maximum of x and y ”

Synthesis via Counterexample-Guided Instantiation

$\neg \exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y \wedge (f(x, y) = x \vee f(x, y) = y)$

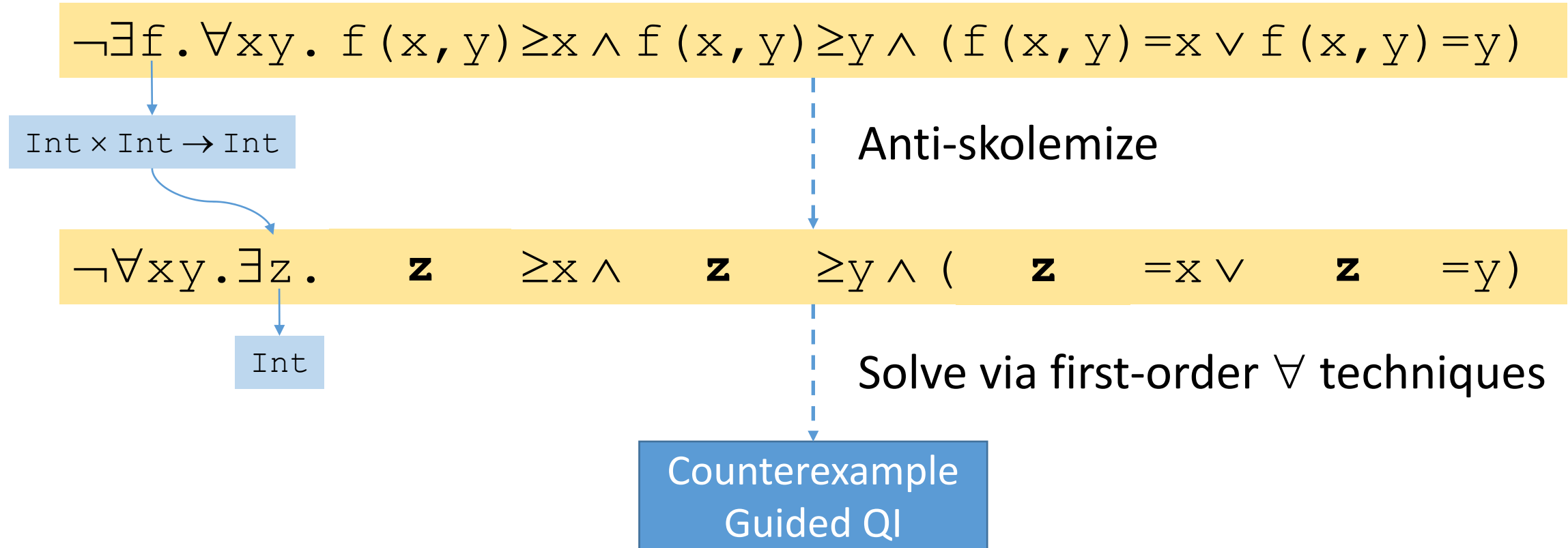
Int \times Int \rightarrow Int

Anti-skolemize

$\neg \forall x y. \exists z. z \geq x \wedge z \geq y \wedge (z = x \vee z = y)$

Int

Synthesis via Counterexample-Guided Instantiation



Counterexample-Guided \forall -Instantiation

Quantifier Elimination Procedures

$\Leftarrow(\Rightarrow)?$

Instantiation-Based procedures for FO $\exists\forall$ formulas

\Leftrightarrow

Synthesis procedures for single-invocation properties

Counterexample-Guided \forall -Instantiation: Caveats

1. Specification must be *single invocation*

- e.g. where functions-to-synthesize are applied to the list of universal variables

✓ $\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) \geq y$

✓ $\exists f g. \forall x. f(x) = g(x)$

✗ $\exists f. \forall x y. f(x, y) = f(y, x)$

2. If syntax restrictions are present, CEGQI may violate them

- Heuristic fitting of solution from CEGQI [[Reynolds et al CAV2015](#)]

3. A term selection strategy must be known for the theory

✓ Linear arithmetic, small finite domains, BV, ...

✗ Strings, non-linear arithmetic, ...

Approach #2: Smart Enumerative SyGuS

Smart Enumerative SyGuS

Conjecture

$$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$$

Syntactic Restrictions

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
       ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | =(fInt, fInt)
```

Smart Enumerative SyGuS

Conjecture

$$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$$

Inductive Datatype

~~Syntactic Restrictions~~

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := >(fInt, fInt) | =(fInt, fInt)
```

View syntactic restrictions as an *inductive datatypes*

Smart Enumerative SyGuS

Conjecture

$$\exists \mathbf{f}. \forall x y. \mathbf{f}(x, y) \geq x \wedge \mathbf{f}(x, y) = \mathbf{f}(y, x)$$

$\text{Int} \times \text{Int} \rightarrow \text{Int}$

$$\exists \mathbf{d}. \forall x y. E(\mathbf{d}, x, y) \geq x \wedge E(\mathbf{d}, x, y) = E(\mathbf{d}, y, x)$$

\mathbf{fInt}

Encode using *deep embedding* involving \mathbf{fInt}

“Evaluation function” $E : \mathbf{fInt} \times \text{Int} \times \text{Int} \rightarrow \text{Int}$

Inductive Datatype

```
 $\mathbf{fInt} := \mathbf{x} \mid \mathbf{y} \mid \mathbf{0} \mid \mathbf{1} \mid +(\mathbf{fInt}, \mathbf{fInt}) \mid$   
 $\quad \mathbf{ite}(\mathbf{fBool}, \mathbf{fInt}, \mathbf{fInt})$   
 $\mathbf{fBool} := \geq(\mathbf{fInt}, \mathbf{fInt}) \mid =(\mathbf{fInt}, \mathbf{fInt})$ 
```

Smart Enumerative SyGuS

Conjecture

$\exists f. \forall x y. f(x, y) \geq x \wedge f(x, y) = f(y, x)$

$\exists d. \forall x y. \mathbf{E}(d, x, y) \geq x \wedge \mathbf{E}(d, x, y) = \mathbf{E}(d, y, x)$

Smart Enumerative SyGuS

Inductive Datatype

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := >= (fInt, fInt) | =(fInt, fInt)
```

Solve via datatypes theory solver + CEGIS
Models for $d \Leftrightarrow$ candidate solutions

Pruning via Theory Rewriting

Conjecture

$\exists d. \forall x y. E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x)$

Inductive Datatype

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
       ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | =(fInt, fInt)
```

Pruning via Theory Rewriting

Conjecture

$\exists d. \forall x y. E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x)$

Inductive Datatype

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | =(fInt, fInt)
```

- Solver generates a stream of candidate models:

- $d^M = x$
- $d^M = y$
- $d^M = +(1, y)$
- $d^M = +(0, x)$
- $d^M = +(y, 1)$
- ...

Pruning via Theory Rewriting

Conjecture

$\exists d. \forall x y. E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x)$

Inductive Datatype

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := ≥(fInt, fInt) | =(fInt, fInt)
```

- Solver generates a stream of candidate models:

- $d^M = x$
- $d^M = y$
- $d^M = +(1, y)$
- $d^M = +(0, x)$
- $d^M = +(y, 1)$

Optimization: *Only consider terms d^M whose **analog** is unique up to theory-specific simplification* ↓

Pruning via Theory Rewriting

Conjecture

$\exists d. \forall x y. E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x)$

Inductive Datatype

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := >=(fInt, fInt) | =(fInt, fInt)
```

- Solver generates a stream of candidate models, normalizes values \downarrow :

• $d^M =$	x	...	x	$=\downarrow$	x
• $d^M =$	y	...	y	$=\downarrow$	y
• $d^M =$	+(1, y)	...	1+y	$=\downarrow$	y+1
• $d^M =$	+(0, x)	...	0+x	$=\downarrow$	x
• $d^M =$	+(y, 1)	...	y+1	$=\downarrow$	y+1

Pruning via Theory Rewriting

Conjecture

$\exists d. \forall xy. E(d, x, y) \geq x \wedge E(d, x, y) = E(d, y, x)$

Inductive Datatype

```
fInt := x | y | 0 | 1 | +(fInt, fInt) |  
      ite(fBool, fInt, fInt)  
fBool := >=(fInt, fInt) | =(fInt, fInt)
```

- Solver generates a stream of candidate models, normalizes values ↓:

• $d^M =$	x	...	x	$\Rightarrow \downarrow$	x	
• $d^M =$	y	...	y	$\Rightarrow \downarrow$	y	
• $d^M =$	+(1, y)	...	1+y	$\Rightarrow \downarrow$	y+1	
• $d^M =$	+(0, x)	...	0+x	$\Rightarrow \downarrow$	x	
• $d^M =$	+(y, 1)	...	y+1	$\Rightarrow \downarrow$	y+1	

Avoid candidate solutions not unique up to theory normalization

Syntactic Constraints in Smart Enumerative SyGuS

$$\neg \text{is}_+(d) \vee \neg \text{is}_x(d.1) \vee \neg \text{is}_0(d.2)$$

“Do not consider solutions where d is $+(x, 0)$ ”

- Encoding uses *shared selectors* of the form $d.1$
 - Agnostic to constructor of d [Reynolds et al IJCAR 2018]

- Syntactic constraints can be generalized:

$$\neg \text{is}_+(d) \vee \neg \text{is}_0(d.2)$$

“Do not consider solutions where d is of the form $+(t, 0)$ for any t ”

⇒ Leads to stronger search space pruning [Reynolds et al CAV 2019]

(Partial) Evaluation Unfolding

$$\text{is}_{\text{ite}}(d) \Rightarrow E(d, x, y) = \text{ite}(E(d.1, x, y), E(d.2, x, y), E(d.3, x, y))$$

“When the top symbol of d is **ite**, its evaluation behaves like if-then-else”

$$\text{is}_+(d) \wedge \text{is}_x(d.1) \wedge \text{is}_1(d.2) \Rightarrow E(d, x, y) = x+1$$

“When d has value **$x+1$** , its evaluation is equal to $x+1$ ”

- Evaluation unfolding lemmas connect evaluation symbols E to theory
- Implementation combines partial and total unfolding
 - Boolean connectives and ITE use partial unfolding, others use total

Approach #3: Fast Enumerative SyGuS

Fast Enumerative SyGuS

$\exists f . \forall x . \Psi$

Fast Enumerative SyGuS

FASTENUM(τ, k):

For all:

- Constructor classes $C \in \mathcal{C}_\tau$, whose elements have type $\tau_1 \times \dots \times \tau_n \rightarrow \tau$,
 - Tuple of naturals (k_1, \dots, k_n) such that $k_1 + \dots + k_n + \text{ite}(n > 0, 1, 0) = k$,
- (a) Run FASTENUM(τ_i, k_i) for each $i = 1, \dots, n$,
- (b) Add $C(t_1, \dots, t_n)$ to S_τ^k for all tuples (t_1, \dots, t_n) with $t_i \in S_{\tau_i}^{k_i}$ and all constructors $C \in \mathcal{C}$.

```
(sygus-enum 0)
(sygus-candidate (max 0))
(sygus-enum 0)
(sygus-enum 1)
(sygus-enum x)
(sygus-enum x)
(sygus-candidate (max x))
(sygus-enum x)
(sygus-enum y)
(sygus-enum y)
(sygus-candidate (max y))
(sygus-enum y)
(sygus-enum (+ x x))
(sygus-enum (+ x 1))
(sygus-enum (+ 1 1))
(sygus-enum (+ 1 y))
(sygus-enum (+ y y))
(sygus-enum (+ y x))
(sygus-enum (- 1 x))
(sygus-enum (- 1 y))
(sygus-enum (- y 1))
(sygus-enum (- 0 1))
(sygus-enum (- x 1))
(sygus-enum (- y x))
(sygus-enum (- 0 y))
(sygus-enum (- x y))
(sygus-enum (- 0 x))
(sygus-enum (+ (+ x x) 1))
(sygus-enum (+ (+ x 1) 1))
```

- Directly enumerate terms based on custom iterator data structures

Fast Enumerative SyGuS (vs. Smart)

PROS:

- Can use (basic) theory rewriting to prune redundant terms
- Very fast
 - Roughly 100x term throughput w.r.t smart enumeration

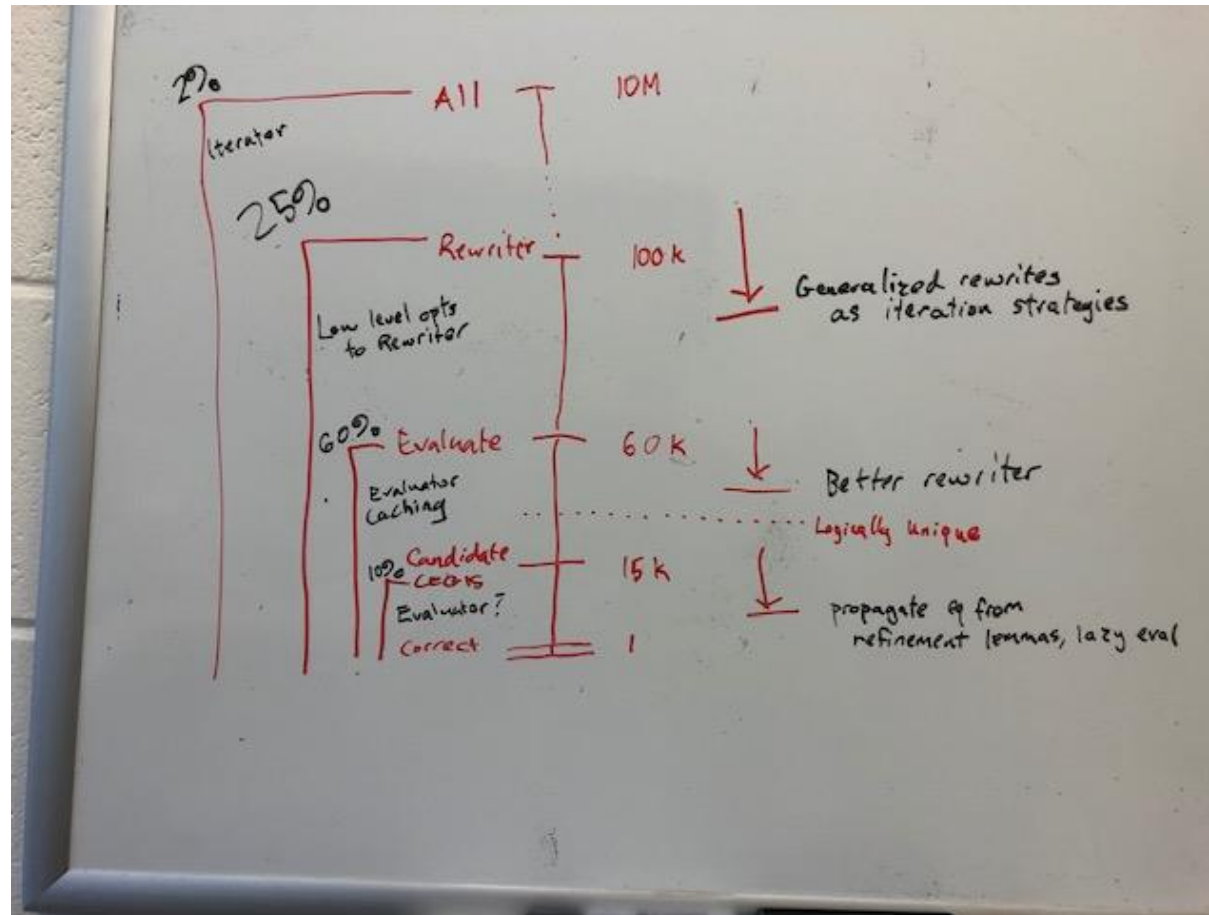
CONS:

- Cannot easily generalize syntactic constraints
 - Thus, more advanced pruning techniques are not (easily) applicable

SUMMARY:

- Fast is usually better, smart is required for harder conjectures [\[Reynolds et al CAV 2019\]](#)

Profiling Fast Enumerative SyGuS



- Evaluating terms on concrete examples is the bottleneck (~70% of runtime)

Summary of Solvers

- If Ψ is single invocation, no grammar restrictions, has theory QI
 - (#1) Use *counterexample-guided quantifier instantiation*
- Else:
 - If Ψ has multiple function-to-synthesize or grammar with Bool connectives
 - (#2) Use *smart enumerative SyGuS*
 - Else:
 - (#3) Use *fast enumerative SyGuS*

Solvers are Supplemented with Additional Techniques

- For CEGQI:
 - Partial quantifier elimination as a preprocessing pass
 - Heuristic solution reconstruction
- For enumerative:
 - Divide-and-conquer [\[Alur et al 2017\]](#)
 - Piecewise-Independent Unification (UNIF+PI) [\[Barbosa et al FMCAD2019\]](#)
 - Theory-specific constant repair [\[Abate et al 2019\]](#)
 - Static grammar minimization and symmetry breaking
 - Variable agnostic enumeration

Ongoing work

- Internal use of SyGuS *for improving the SMT solver*
 - For designing QI algorithms [Niemetz et al CAV 2018, Brain et al CAV2019]
 - Discovering rewrite rules [Noetzli et al SAT 2019]
 - User-guided test case generation
 - Quantifier instantiation via enumerative SyGuS [Niemetz et al TACAS 2021]
- Algorithms that utilize enumerative *SyGuS as a black box*
 - Invariant synthesis [Barbosa et al FMCAD 2019]
 - Abduction [Reynolds et al IJCAR 2020]
 - Interpolation
 - Optimization
- Low-level optimizations

Thanks!

- SyGuS techniques in talk available in SMT solver CVC4(...5)
 - Open-source : <https://cvc4.github.io/>
 - Includes Python and C++ APIs for SyGuS
 - Java API coming soon
- Questions?

